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PATENT
TESSERA 3.3-018

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of :
KHANDROS ET AL. :
Serial No.: 08/030,194 :
Filed: April 28, 1993 :
For: SEMICONDUCTOR CHIP :
ASSEMBLIES, METHODS OF :
MAKING SAME AND COMPONENTS :
FOR SAME :
X

Group Art Unit: 2164
Examiner: D. Russele
Date: February 27, 1995

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AS

Hon. Commissioner of Patents
and Trademarks
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

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GROUP 2164

Sir:

It is respectfully requested that the references listed on the enclosed Form PTO-1449 be made of record and considered with respect to the above-referenced U.S. Patent application. A copy of each reference is enclosed. Submission of the present Information Disclosure Statement should not be taken as an admission that the cited references are legally available prior art or that the same are pertinent or material.

Respectfully submitted,

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